

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10604491
Filing Date	2003-07-25
First Named Inventor	Christos J. Georgiou, et al.
Art Unit	2111
Examiner Name	Kenneth S. Kim
Attorney Docket Number	FIS920030113US1 (16497)

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	2	5056058		1991-10-08	Hirata et al.	
	3	5229994		1993-07-20	Balzano et al.	
	4	5535373		1996-07-09	Olnowich	
	5	5586273		1996-12-17	Blair et al.	
	6	5931933		1999-08-03	Billheimer et al.	
	7	6055581		2000-04-25	Berglund et al.	
	8	6678535		2004-01-13	Narayanaswami	

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	1	Almasi, et al., "Dissecting Cyclops: A Detailed Analysis of a Multithreaded Architecture", RC 22243 (98713) 01APR2002, pp. 1-15	<input type="checkbox"/>
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	3	IBM Corporation: "IBM introduces PowerPC 440 embedded processor", http://www-3.ibm.com/chips/news/2003/0922_440ep.html ; last printed 9/20/06; pp. 1-2	<input type="checkbox"/>

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4	IBM Corporation: "CoreConnect bus architecture", http://www-3.ibm.com/chips/products/coreconnect/ ; last printed 9/20/06; pp. 1-2	<input type="checkbox"/>
5	ARM: "Processor Cores Overview", http://www.arm.com/armtech/cpus?OpenDocument ; last printed 9/20/06; pp. 1-2	<input type="checkbox"/>
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☐ See attached certification statement.

☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Steven Fischman/	Date (YYYY-MM-DD)	2006-09-29
Name/Print	Steven Fischman	Registration Number	34,594

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